



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Marc HOURDEQUIN et al.

Serial No.: 09/891,499

Group Art Unit: Unassigned

Filed: June 27, 2001

Examiner: Unassigned

For: DEVICES CONTAINING LOGIC CIRCUITS  
TO GENERATE RANDOM SIGNALS

PRELIMINARY AMENDMENT

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination of the above-identified application,  
please enter the following specification changes as noted below:

IN THE CLAIMS:

Please amend claims 7 and 11 as follows:

7. (Amended) Device as claimed in claim 5, characterized  
in that the means constituting the inverter comprise a plurality  
in the form of an odd number of inverters (111).

11. (Amended) Device as claimed in claim 1, characterized  
in that it is incorporated into a programmable integrated  
circuit.

REMARKS

Claims 1-11, as amended, remain herein. Claims 7 and 11 have been amended hereby.

This Preliminary Amendment is submitted to eliminate multiply dependent claims from the above-identified application.

Examination of this application on its merits is respectfully requested.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.



Roger W. Parkhurst  
Registration No. 25,177

February 22, 2002  
Date

Attachment:

Mark Up of Amended Claims

RWP/ame

Attorney Docket No. DPAG:037

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7. Device as claimed in either of claims 5 and 6  
claim 5, characterized in that the means constituting the  
inverter comprise a plurality in the form of an odd number  
of inverters (111).

11. Device as claimed in one of the above claims claim  
1, characterized in that it is incorporated into a specific  
or into a programmable integrated circuit.

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